Extreme fast charging: Identification of failure modes and routes to improve performance

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### **Energy Storage and Electric Transportation at INL**



Battery Evaluation Lab (NOBEL) Infrastructure Laboratory

(EVIL)

Kim et al., *Energy Storage Materials* (2022) Chinnam et. al ACS Applied Energy Materials (2021)

#### **Battery Aging Due to Fast Charge**





- Li plating is typically only partially reversible
- Results in electrically isolated Li Eventual loss of capacity and performance

## **AGING AT THE POSITIVE ELECTRODE**

#### NMC532 and 811 post-test: Aging mechanisms

- Interprimary particle separation (IPPS or cracking) with distinct evolution.
- NMC811 better at retaining performance enhanced electronic conductivity
- Bulk structure remained layered. More surface reconstruction at higher C-rates.



Tanim et. al. *Energy Storage Materials (2021)* Tanim et al. *Adv. Energy Materials (2022)* 

Post-tests were performed at Argonne





Use understanding of cell design to develop protocols and rapid understanding of failure modes

### **Increasing Cell Energy**

- Electrode loading increased by 50% (from 2 to 3 mAh/cm<sup>2</sup>)
- Max current decreased but overall protocol structure maintained





#### Chinnam et al, ACS Appl. Energy Mat., (2021)

# Advancing charge acceptance during short duration charging

- Reduced time during CV portion of the charge
- Same negative electrode loading
- Able to increase charge acceptance 10+%



 Key changes – <u>Charge protocol</u>, <u>electrolyte</u>, cathode material (NMC811 vs NMC532)

Tanim et al, Cell Reports Phys. Sci, 2020, 100114 Chinnam et al, ACS Appl. Energy Mat., 2021, 9133

#### **Advanced Charge profiles**

#### Charge protocols – Can be readily tailored to specific builds with early characterization

- Voltage Ramp Goal to minimize Li plating by maintaining negative electrode potential above 0V vs Li
- Material Stress Reduction (MSR) Reduced stress on materials by ramp to higher current. Current determined based on cell overvoltage and impedance characteristics
- Protocols developed to minimize plating and align with infrastructure design





(b) Voltage Ramp (VR)



#### (c) Material Stress Reduction (MSR)



#### Kim et al, Energy Technology, 2022, 2200303

### INL's approach: aging features + data-driven



<sup>9</sup> Chen et al, Cell Reports Phys Sci, 2021, 100352



#### 

### Lots of battery data are needed to cover aging cases

#### To identify other aging modes and complex pathways:

- Extensive amounts of different aging cases are needed
- Difficult to experimentally cover all cases
- "Simulated battery" → user control what type and how much aging present and generate "synthetic data"

#### Electrode potential curves shifts due to aging, then IC curves are obtained



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### **Deep learning advances the aging detection framework**



Kim et al, Energy Storage Materials (2022) 12

#### **Testing on Experimental Data - Classification**



A 2D visualization map using *t*-SNE for both synthetic data sets and DL-classified experimental data, with two loading cases: (a) low-loading and (b) moderate-loading under two dominant aging modes—i.e., LLI + LAM<sub>PE</sub> and LLI + LAM<sub>NE</sub>.

### **Validation of Aging Constituents**



DL framework successfully validated with experimental data

#### **Applied to most recent results**

# Rapid Ability to Transfer Learning from NMC532 to 811

- Like earlier work can readily classify and quantify extent of degradation modes
- Looked at LLI and LAM (not shown)
- Close alignment in use of synthetic data which can be readily transitioned across chemistries and cell design types
- Still working to understand ability to classify variations in cell design
- Only a few instances of Li plating detected using decision tree analysis





### Summary

- Ability to achieve 90+% charge acceptance possible due to use of advanced protocols and electrolytes
- Use of ML methods developed to identify Li plating and classify and quantify degradation modes
  - Use of synthetic data enables direct flexibility in expanding methods beyond use of experimental data
- Failure modes can be used for future development of protocols

### **Contributors, Collaborators and Acknowledgements**

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